



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: FADI Y. BUSABA et al. )  
Serial No.: 10/616,556 ) Group Art Unit: 2124  
Filed: July 10, 2003 )  
For: DECIMAL MULTIPLICATION ) Examiner: NYA  
          USING DIGIT RECODING )  
                                  ) Confirmation No.: 2800

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §§ 1.56, 1.97 AND 1.98

The Commissioner of Patents  
and Trademarks  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty to disclose, applicants submit herewith a copy of each publication, excluding U.S. Patents and U.S. published patent applications, of which those designated by 37 CFR § 1.56 are aware; the publications are listed on attached form PTO-1449. The present Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified on the attached form PTO-1449 exists.

Consideration of this Information Disclosure Statement is respectfully requested, since the Examiner may consider the information provided herein material to the patentability of the subject application as defined in 37 CFR § 1.56.

Since this Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits, no fee or certification under 37 CFR § 1.97 (e) is required.

However, in the event the Commissioner of Patents deems that any fee is required under 37 CFR §§ 1.16 and 1.17 in connection with this application, applicants' attorneys authorize that such fee be charged to Deposit Account No. 09-0463.

Respectfully submitted,

FADI Y. BUSABA ET AL.

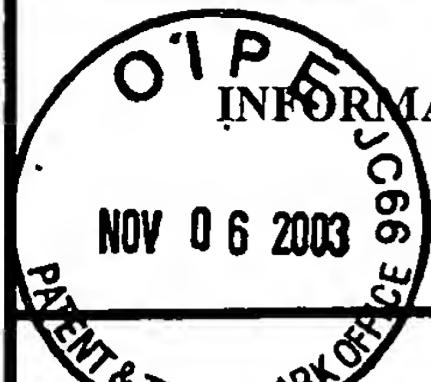
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**INFORMATION DISCLOSURE CITATION**  
*(Use several sheets if necessary)*

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NOV 06 2003

Docket Number (Optional) <b>POU920030052US1</b>	Application Number <b>10/616,556</b>
Applicant(s) <b>FADI Y. BUSABA, ET AL.</b>	
Filing Date <b>July 10, 2003</b>	Group Art Unit <b>2124</b>

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

**OTHER DOCUMENTS** (*Including Author, Title, Date, Pertinent Pages, Etc.*)

	7	"Packed Decimal Multiply Algorithm": Inventor: Hoffman, RL Schardt, TL: Pubname: TDB 10-75 pgs. 1562-1563; Disclosure No. RO8750134: October 1975
	8	"High Performance Two Cycle Loop Decimal Multiply Algorithm"; Inventors: Angiulli, JM Chang, DC Hornick, JC Nohilly, WJ Zajac, MW: Pubname: TDB 09-81 pgs. 1845-1849; Disclosure No. PO8800209: September 1981

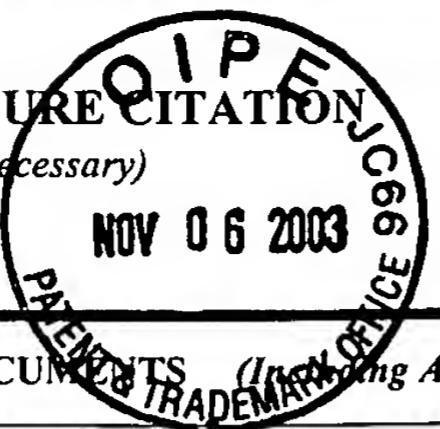
**EXAMINER** \_\_\_\_\_ **DATE CONSIDERED** \_\_\_\_\_

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INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

IBM Info Gate; "Improved Table Assisted Addition and Multiplication Methods"; Inventor: Wingert, JA; Pubname: TDB 02-83 pgs. 4742-4743; Disclosure No. CT8800050; February 1983

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IBM Info Gate; "High Speed Binary and Decimal Multiply"; Inventor: Singh, S. Weinberger, A; Pubname: TDB 05-76 pgs. 4105-4106; Disclosure No. PO8750129; May 1976

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"Decimal Adder with Signed Digit Arithmetic"; Inventor: Antonin Svoboda; Ieee Transactions on Computers, Vol. C-18, No. 3, pgs. 212-215, March 1969

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"A Signed Binary Multiplication Technique"; Inventor: Andrew D. Booth; Oxford University Press; Q.J. Mech. Appl. Math 4:236-240, pgs. 100-104, 1951

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